

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE					ATTY. DOCKET NO. TI-31245	SERIAL NO. 10/001,707	
LIST OF DOCUMENTS CITED BY APPLICANT <i>(Use several sheets if necessary)</i>					APPLICANT Theodore W. Houston		
					FILING DATE 10/25/01	GROUP TBD	
U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE (IF APPROPRIATE)
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION Yes      No
	BA						
	BB						
	BC						
	BD						
	BE						
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
MP	CA	Takeda, et al. "A 16-Mb 400-MHz Loadless CMOS Four-Transistor SRAM Macro", IEEE Journal of Solid-State Circuits, Vol. 35, No. 11, November 2000, pp. 1631-1640.					
	CB						
	CC						
	CD						
	CE						
EXAMINER <i>[Signature]</i>				DATE CONSIDERED 7/06			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							